Date

: 4/21/2009

Patent No.

: 7,032,206 B2

Serial No.

: 10/647,688

Inventor(s)

: Keller et al.

Issue Date

: April 18, 2006

Title

: SYSTEM AND METHOD FOR ITERATIVELY TRAVERSING

A HIERARCHICAL CIRCUIT DESIGN

Doc./File No.

: 100111238-1 -

Re: Certificate of Correction (Reconsideration)

Consideration has been given your request for a certificate of correction (reconsideration), for the above-identified patent under the provisions of Rule 1.322 and 1.323.

Respecting the alleged errors in your request, inspection of the file of the application for the patent reveals that in column 4, line 26, that there is no discrepancy.

Further consideration will be given concerning this matter upon receipt of a request for **Reconsideration** (reconsideration should be accompanied by supporting document(s) such as, amendment, postcard receipt, 1449/892, etc.) and should be filed and directed to Decisions & Certificates of Correction Branch.

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Mary F. Diggs, Supervisor (703) 308-9390 x125 Decisions & Certificates of Correction Branch

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